

**AMENDMENTS TO THE SPECIFICATION
IN THE CROSS REFERENCE TO RELATED APPLICATIONS**

Please replace paragraph [0001] through [0003] "Cross Reference to Related Application" current text with that shown below. All amendments are shown with deleted text struckthrough or double bracketed and new text underlined.

CROSS REFERENCE TO RELATED APPLICATIONS

[0001] This application is related to U.S. Patent Application Serial No. 09/803,349 filed concurrently herewith;

by inventors Joel W. Page, Trenton J. Grale, Zhuan Ye, Erng Sing Wee, Sumant Sathe and Sijan Chen, entitled "SIGNAL PROCESSING INTEGRATED CIRCUIT" (Docket No. 1120-CS) P154US, currently pending.

A1 [0002] This application is related to U.S. Patent Application Serial No. 09/802,744 filed concurrently herewith, by inventors Joel W. Page, Wai Laing Lee, and Erng Sing Wee, entitled "INTEGRATED CIRCUIT ARRANGEMENT FOR MULTIPLE-SENSOR TYPES WITH SELECTABLE FRONT ENDS" (Docket No. 1077-CS P253US) currently pending.

[0003] This application is related to U.S. Patent Application Serial No. 09/803,222, filed concurrently herewith, by inventor Trenton J. Grale, entitled "LOW-POWER LOW-AREA SHIFT REGISTER " (Docket No. 1076 P152US) granted August 13, 2002 under U.S. Patent 6,434,231.

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